

SJD12C05LAM Transient Voltage Suppressor

Description

The SJD12C05LAM ESD protector is designed to replace multilayer varistors (MLVs) in portable applications such as cell phones, notebook computers, and PDA's. They feature large cross-sectional area junctions for conducting high transient currents, offer desirable electrical characteristics for board level protection, such as fast response time, lower operating voltage, lower clamping voltage and no device degradation when compared to MLVs. The SJD12C05LAM protects sensitive semiconductor components from damage or upset due to electrostatic discharge (ESD) and other voltage induced transient events. The SJD12C05LAM is available in a SOD-123FL package with working voltages of 7volt.



Feature

- 2400W Peak pulse power per line (t_P = 8/20µs)
- SOD-123FL package
- Response time is typically < 1 ns</p>
- Protect one I/O or power line
- Low clamping Voltage
- RoHS compliant
- Transient protection for data lines to IEC 61000-4-2(ESD)
 ±30KV(air), ±30KV(contact); IEC 61000-4-4 (EFT) 40A (5/50ns)

Applications

- > Cell phone handsets and accessories
- Personal digital assistants (PDA's)
- Notebooks, desktops, and servers
- Portable instrumentation
- Cordless phones
- Digital cameras
 - Peripherals
 - MP3 players

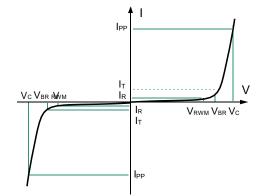
Mechanical Characteristics

- Lead finish:100% matte Sn(Tin)
- Mounting position: Any
- Qualified max reflow temperature:260°C
- Device meets MSL 1 requirements
- Pure tin plating: 7 ~ 17 um
- ➢ Pin flatness∶≤3mil



Electronics Parameter

Symbol	Parameter		
V _{RWM}	Peak Reverse Working Voltage		
I _R	Reverse Leakage Current @ V _{RWM}		
V _{BR}	Breakdown Voltage @ I⊤		
Ι _Τ	Test Current		
Ірр	Maximum Reverse Peak Pulse Current		
Vc	Clamping Voltage @ IPP		
P _{PP}	Peak Pulse Power		
CJ	Junction Capacitance		



Electrical characteristics per line@25°C(unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
Peak Reverse Working Voltage	V _{RWM}			5		V
Breakdown Voltage	V _{BR}	I _t =1mA		7.8		V
Reverse Leakage Current	IR	V _{RWM} =7V			200	μA
Clamping Voltage	Vc	I _{PP} =145A t _P = 8/20µs		16.5	19	V
Junction Capacitance	Cj	V _R =0V f = 1MHz		480	490	pF

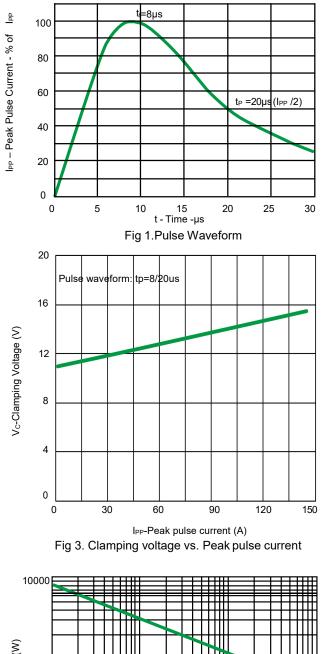
Absolute maximum rating@25℃

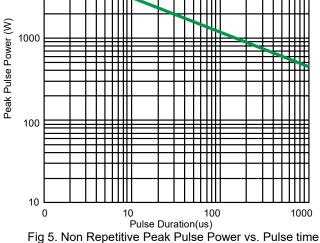
Rating	Symbol	Value	Units
Peak Pulse Power(t₂ = 8/20µS)	P _{pp}	2400	W
Lead Soldering Temperature	TL	260 (10 sec)	°C
Operating Temperature	TJ	-55 to +150	°C
Storage Temperature	Тѕтс	-55 to +150	°C

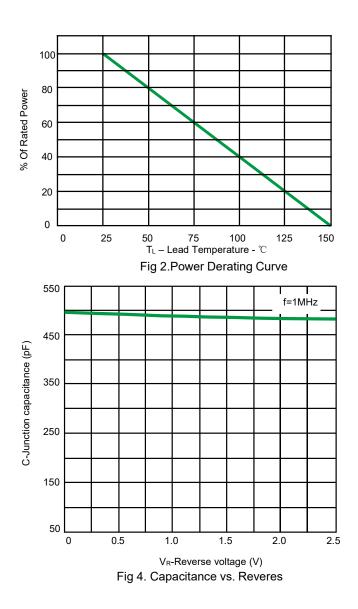


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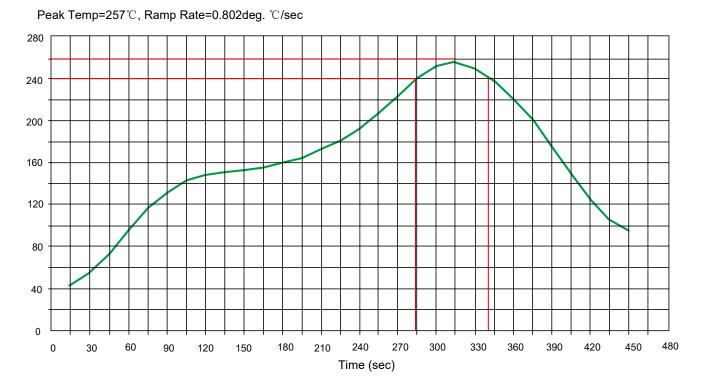
Typical Characteristics











Solder Reflow Recommendation

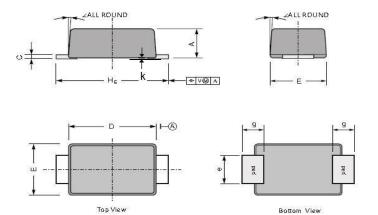
PCB Design

For TVS diodes a low-ohmic and low-inductive path to chassis earth is absolutely mandatory in order to achieve good ESD protection. Novices in the area of ESD protection should take following suggestions to heart:

- > Do not use stubs, but place the cathode of the TVS diode directly on the signal trace.
- > Do not make false economies and save copper for the ground connection.
- Place via holes to ground as close as possible to the anode of the TVS diode.
- Use as many via holes as possible for the ground connection.
- > Keep the length of via holes in mind! The longer the more inductance they will have.

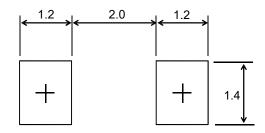


Product dimension (SOD-123FL)



Unit:mm

Dim	Inches		Millimeters		
	MIN	MAX	MIN	МАХ	
A	0.031	0.047	0.80	1.20	
С	0.002	0.010	0.05	0.25	
HE	0.138	0.154	3.50	3.90	
E	0.061	0.077	1.55	1.95	
D	0.098	0.114	2.50	2.90	
g	0.020	0.043	0.50	1.10	
е	0.024	0.039	0.60	1.00	
k	0.0)04	0.10		
2	7 °				



Suggested PCB Layout

Unit:mm

Ordering information



Ordering information

Device	Package	Reel	Shipping
TAPING	SOD-123FL (Pb-Free)	7"	3000 / Tape & Reel

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IMPORTANT NOTICE

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