

Discription

The RSFK9D3NT is designed to protect voltage sensitive components from ESD. Excellent clamping capability, low leakage, and fast response time ,make these parts ideal for ESD protection on designs where board space is at a premium. Because of its small size, it is suited for use in cellular phones, MP3 players, digital cameras and many other portable applications where board space is at a premium.

Applications

- Cellular phones audio
- MP3 players
- Digital cameras
- Portable applicationss
- mobile telephone

Features

- Small Body Outline Dimensions: 0.61 mm x 0.31 mm
- Low Body Height: 0.28 mm
- Low Leakage
- Response Time is Typically < 1 ns
- ESD Rating of Class 3 (> 16 kV) per Human Body Model
- IEC61000-4-2 Level 4 ESD Protection
- These are Pb-Free Devices
- We declare that the material of product compliance with RoHS requirements.

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
IEC 61000-4-2 (ESD) Air discharge Contact discharge		±30 ±30	kV kV
ESD Voltage Per Human Body Model		30	kV
Total Power Dissipation on FR-5 Board (Note 1)	PD	200	mW
@ T _A =25℃			
Junction and Storage Temperature Range	TJ,TSTG	-55 to 150	°C
Lead Solder Temperature – Maximum (10	TL	260	°C
Second Duration)			

Stresses exceeding Maximum Ratings may damage the device. Maximum Rating are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

1. FR-5 = 1.0*0.75*0.62 in.





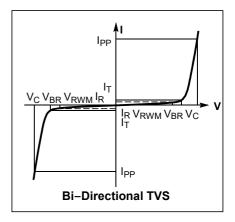
DFN0603-D



ELECTRICAL CHARACTERISTICS

(T_A = 25°C unless otherwise noted)

Symbol	Parameter		
IPP	Maximum Reverse Peak Pulse Current		
Vc	Clamping Voltage @ I _{PP}		
V _{RWM}	V _{RWM} Working Peak Reverse Voltage		
I _R	I _R Maximum Reverse Leakage Current @ V _{RWM}		
V _{BR}	Breakdown Voltage @ I _T		
Ιτ	IT Test Current		
P _{pk}	P _{pk} Peak Power Dissipation		
С	Capacitance @ $V_R = 0$ and f = 1.0 MHz		



ELECTRICAL CHARACTERISTICS

	V _{RWM} (V)	I _{R1} (μΑ) @ V _{RWM}	I R2(µA) @ V _R =3.5∨	V _{BR} (V) @ I _T (Note 2)	Гт	Vc (V) @ IPP = 1 A (Note 3)	Vc (V) @MAX Ipp (Note 3)		Р_{РК}(W) (Note 3)	C (pF)
Device	Max	Max	Мах	Min	mA	Мах	Мах	Мах	Мах	Max
RSFK9D3NT	5.0	0.5	0.3	5.6	1.0	9.8	12.5	5.5	69	15

Other voltage available upon request.

2. V_{BR} is measured with a pulse test current IT at an ambient temperature of 25 $^\circ\!\!\mathbb{C}$

3. Surge current waveform per Figure 3.

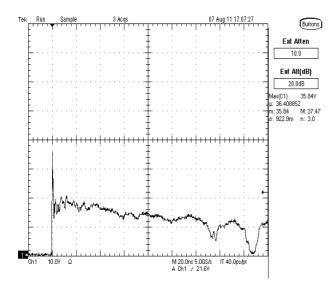


Fig1. ESD Clamping Voltage Screenshot Positive 30kV Contact per IEC61000-4-2

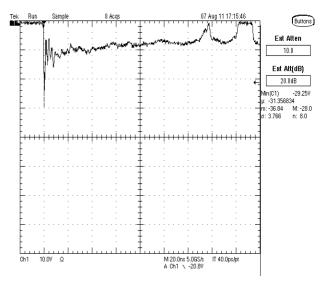


Fig2. ESD Clamping Voltage Screenshot Negative 30kV Contact per IEC61000-4-2



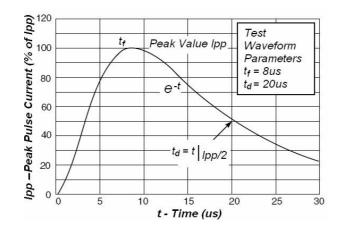
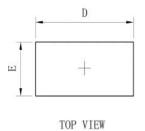


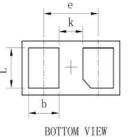
Fig3. Pulse Waveform



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OUTLINE AND DIMENSIONS



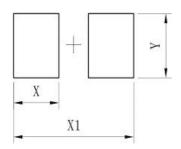


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Dim	Min	Тур.	Max	
D	0.58	0.61	0.66	
E	0.28	0.31	0.38	
е	-	0.34	-	
L	0.20	0.23	0.34	
b	0.16	0.19	0.25	
А	0.25	0.28	0.38	
k	0.12	0.15	0.18	
All Dimensions in mm				



SIDE VIEW

SOLDERING FOOTPRINT



DFN0603-DL		
DIM (mm)		
Х	0.23	
X1	0.58	
Y	0.30	