

### FEATURES

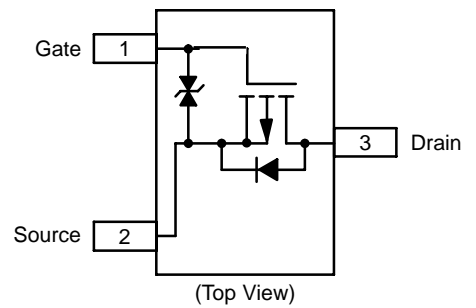
- TrenchFET® Power MOSFET: 1.8-V Rated
- Gate-Source ESD Protected: 2000 V
- High-Side Switching
- Low On-Resistance: 1.2 Ω
- Low Threshold: 0.8 V (typ)
- Fast Switching Speed: 14 ns
- S- Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable.

### BENEFITS

- Ease in Driving Switches
- Low Offset (Error) Voltage
- Low-Voltage Operation
- High-Speed Circuits
- Low Battery Voltage Operation

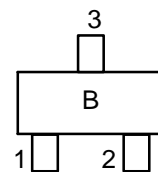
### APPLICATIONS

- Drivers: Relays, Solenoids, Lamps, Hammers, Displays, Memories
- Battery Operated Systems
- Power Supply Converter Circuits
- Load/Power Switching Cell Phones, Pagers



SC-89

### MARKING DIAGRAM



ABSOLUTE MAXIMUM RATINGS (T <sub>A</sub> = 25°C UNLESS OTHERWISE NOTED)					
Parameter		Symbol	5 secs	Steady State	Unit
Drain-Source Voltage		V <sub>DS</sub>	-20		V
Gate-Source Voltage		V <sub>GS</sub>	±6		
Continuous Drain Current (T <sub>J</sub> = 150°C) <sup>b</sup>	T <sub>A</sub> = 25°C	I <sub>D</sub>	-400	-350	mA
	T <sub>A</sub> = 85°C		-300	-275	
Pulsed Drain Current <sup>a</sup>		I <sub>DM</sub>	-1000		
Continuous Source Current (diode conduction) <sup>b</sup>		I <sub>S</sub>	-275	-250	
Maximum Power Dissipation <sup>b</sup> for SC-75	T <sub>A</sub> = 25°C	P <sub>D</sub>	175	150	mW
	T <sub>A</sub> = 85°C		90	80	
Maximum Power Dissipation <sup>b</sup> for SC-89	T <sub>A</sub> = 25°C		275	250	
	T <sub>A</sub> = 85°C		160	140	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	-55 to 150		°C
Gate-Source ESD Rating (HBM, Method 3015)		ESD	2000		V

Notes

- d. Pulse width limited by maximum junction temperature.
- e. Surface Mounted on FR4 Board.

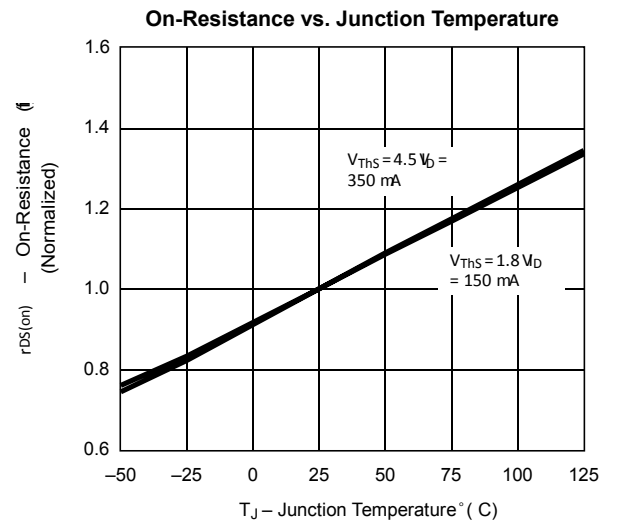
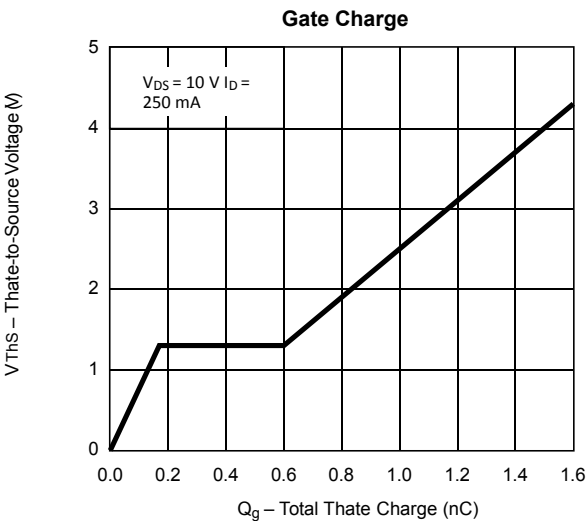
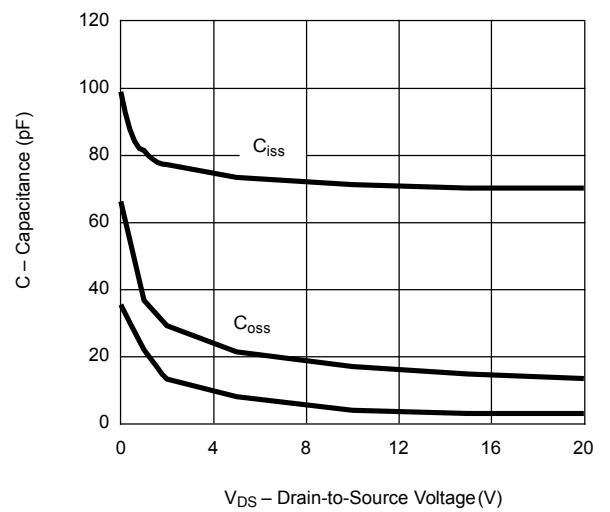
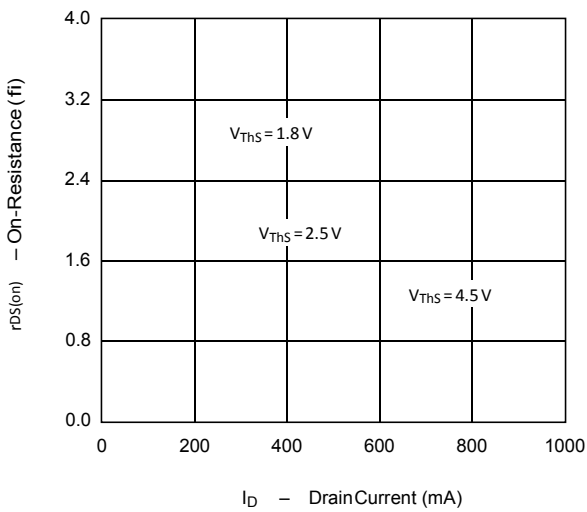
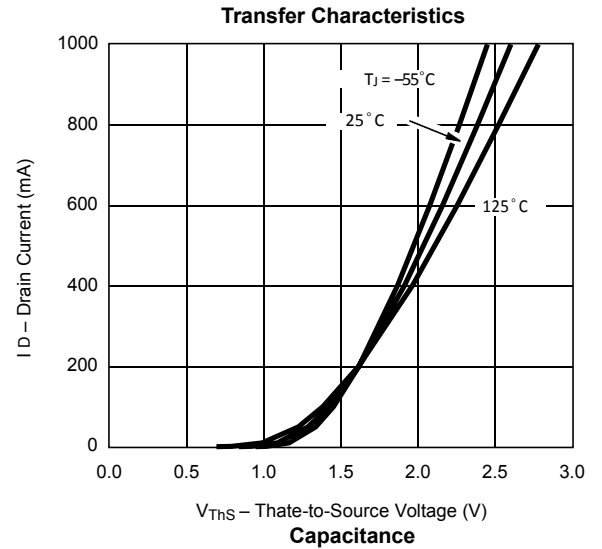
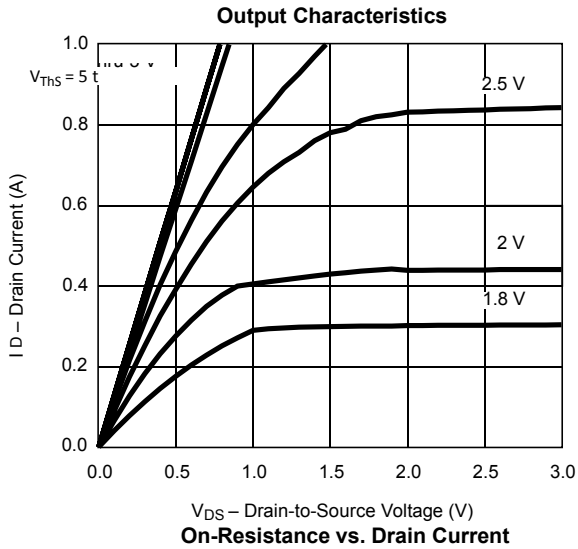
SPECIFICATIONS [T <sub>A</sub> = 25° C UNLESS OTHERWISE NOTED]						
Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
<b>Static</b>						
Thate Threshold Voltage	V <sub>ThS(th)</sub>	V <sub>DS</sub> = V <sub>ThS</sub> , I <sub>D</sub> = -250 μA	-0.45			V
Thate-Body Leakage	I <sub>ThSS</sub>	V <sub>DS</sub> = 0 V, V <sub>ThS</sub> = T4.5 V		T1	T2	μA
Zero Thate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = -16 V, V <sub>ThS</sub> = 0 V		-0.3	-100	nA
		V <sub>DS</sub> = -16 V, V <sub>ThS</sub> = 0 V, T <sub>J</sub> = 85° C			-5	μA
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>DS</sub> = -5 V, V <sub>ThS</sub> = -4.5 V	-700			mA
Drain-Source On-State Resistance <sup>a</sup>	r <sub>DS(on)</sub>	V <sub>ThS</sub> = -4.5 V, I <sub>D</sub> = -350 mA		0.8	1.2	fi
		V <sub>ThS</sub> = -2.5 V, I <sub>D</sub> = -300 mA		1.2	1.6	
		V <sub>ThS</sub> = -1.8 V, I <sub>D</sub> = -10 mA		1.8	2.7	
Forward Transconductance <sup>a</sup>	g <sub>fs</sub>	V <sub>DS</sub> = -10 V, I <sub>D</sub> = -250 mA		0.4		S
Diode Forward Voltage <sup>a</sup>	V <sub>SD</sub>	I <sub>S</sub> = -150 mA, V <sub>ThS</sub> = 0 V		-0.8	-1.2	V
<b>Dynamic<sup>b</sup></b>						
Total Thate Charge	Q <sub>g</sub>	V <sub>DS</sub> = -10 V, V <sub>ThS</sub> = -4.5 V, I <sub>D</sub> = -250 mA		1500		pC
Thate-Source Charge	Q <sub>gs</sub>			150		
Thate-Drain Charge	Q <sub>gd</sub>			450		
Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> = -10 V, R <sub>L</sub> = 47 fi I <sub>D</sub> ÷ -200 mA, V <sub>ThEN</sub> = -4.5 V, R <sub>Th</sub> = 10 fi		5		ns
Rise Time	t <sub>r</sub>			9		
Turn-Off Delay Time	t <sub>d(off)</sub>			35		
Fall Time	t <sub>f</sub>			11		

Notes

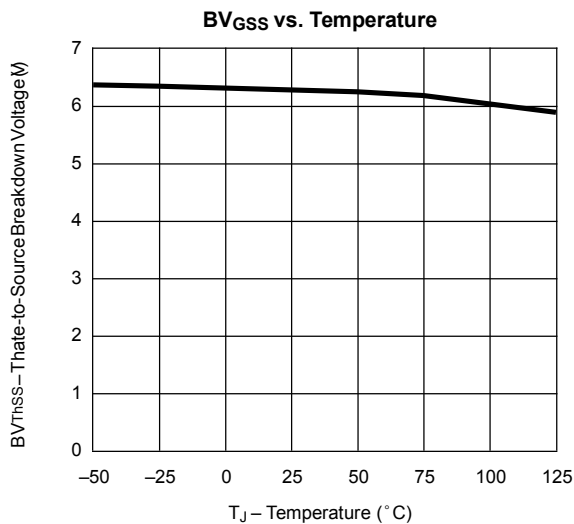
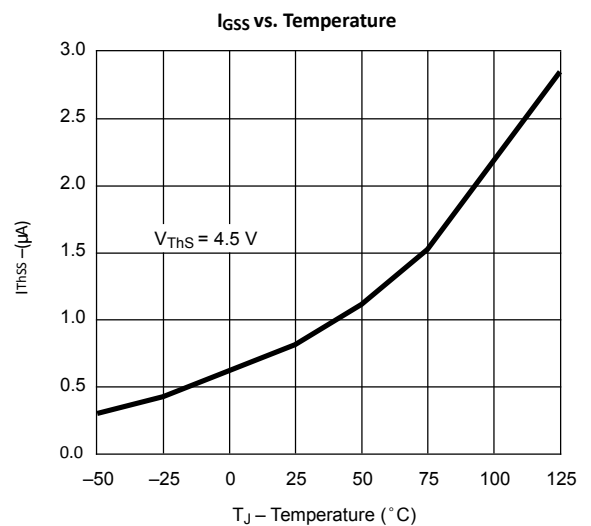
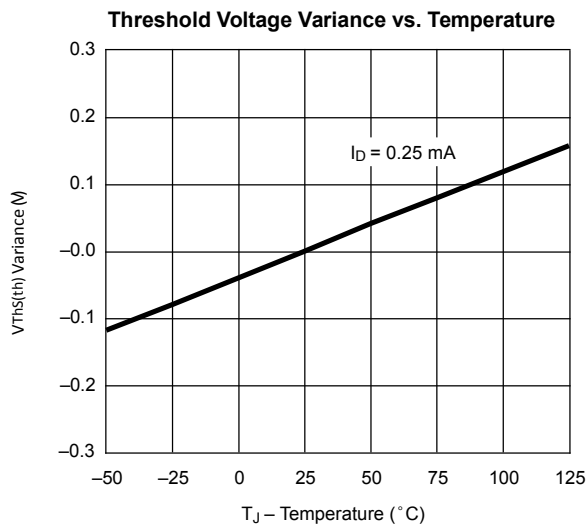
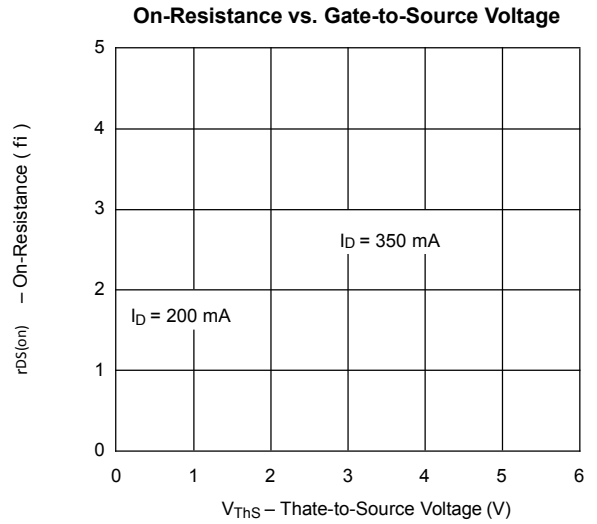
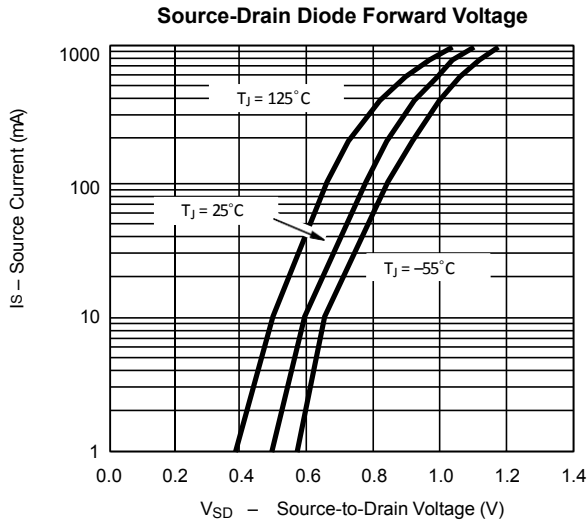
- a. Pulse test; pulse width S 300 μs, duty cycle S 2%.
- b. Thuaranteed by design, not subject to production testing.

**TYPICAL CHARACTERISTICS [T<sub>A</sub> = 25 °C UNLESS NOTED]**

For the following graphs, p-channel negative polarities for all voltage and current values are represented as positive values.

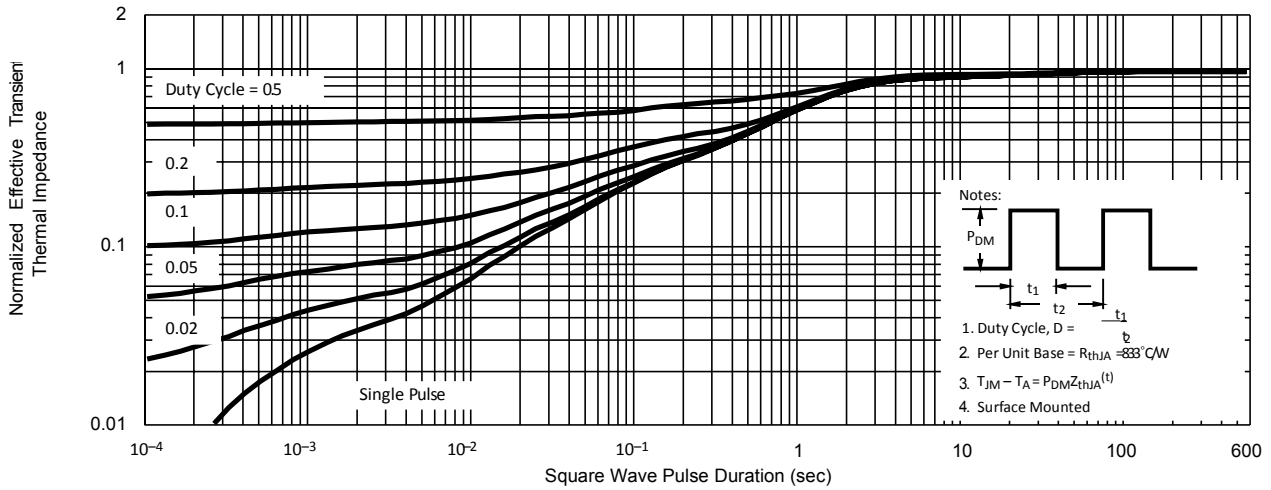


**TYPICAL CHARACTERISTICS [T<sub>A</sub> = 25 °C UNLESS NOTED]**

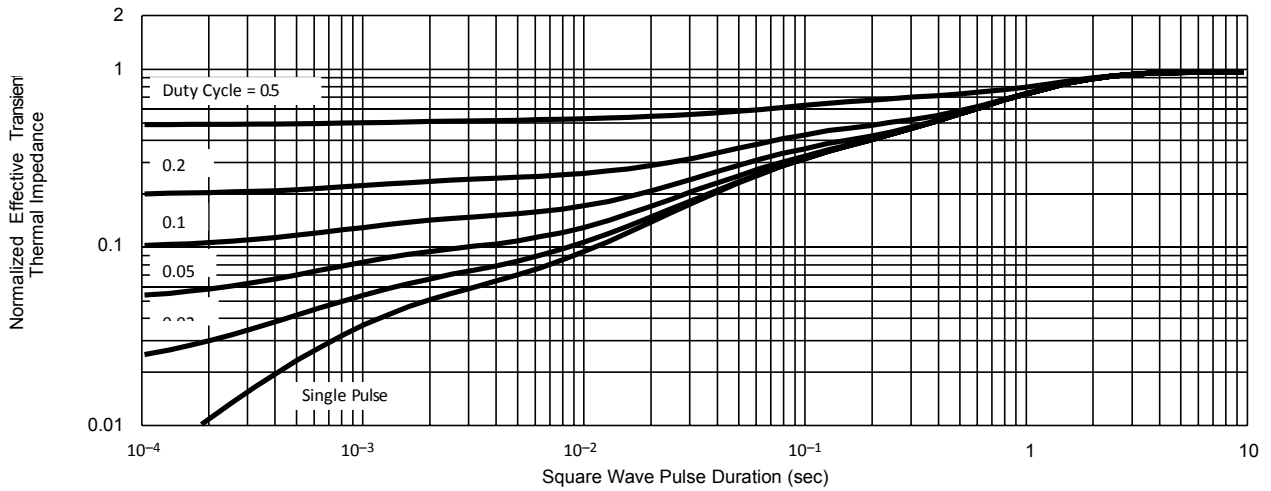


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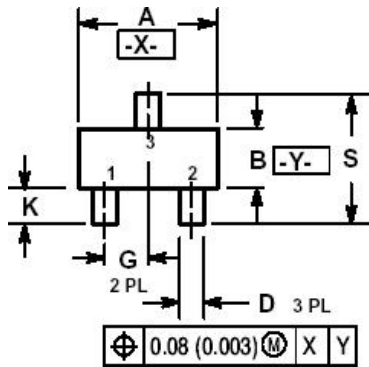
Normalized Thermal Transient Impedance, Junction-to-Ambient (SC-75A)



Normalized Thermal Transient Impedance, Junction-to-Foot

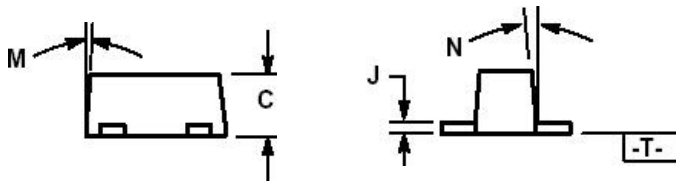


SC-89

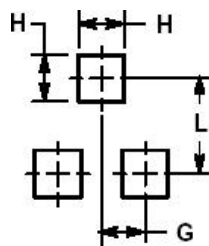


NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: MILLIMETERS
3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.
4. 463C-01 OBSOLETE, NEW STANDARD 463C-02.



DIM	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	1.50	1.60	1.70	0.059	0.063	0.067
B	0.75	0.85	0.95	0.030	0.034	0.040
C	0.60	0.70	0.80	0.024	0.028	0.031
D	0.23	0.28	0.33	0.009	0.011	0.013
G	0.50 BSC			0.020 BSC		
H	0.53 REF			0.021 REF		
J	0.10	0.15	0.20	0.004	0.006	0.008
K	0.30	0.40	0.50	0.012	0.016	0.020
L	1.10 REF			0.043 REF		
M	---	---	10 °	---	---	10 °
N	---	---	10 °	---	---	10 °
S	1.50	1.60	1.70	0.059	0.063	0.067



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